

BUILD AN

Electrolytic Restorer

PREVENT HIGH-VOLTAGE CAPACITOR BREAKDOWN

BY GEORGE J. PLAMONDON

WHEN a high-voltage electrolytic capacitor has been unused for too long a time, the truthermark rounder the long at time, truthermarker. Too often, when power is applied to such units, the delectric punctures, destroying the enquenter and probabily the associated circuit. Unfortunately, many power supplies to these capacitors in their junk boxes (they were quite comman) power supplies the power supplies them. Since they are fairly expensive, it behoves the electronic experimenter or service man to salvage such

capacitors by restoring the dielectric so that there is no chance of its breaking down when put to use.

However, before finding out how to re-

What is an Electrolytic Capacitor? An electrolytic capacitor usually consists of two Besible sheets of abanisms foil separated by gazze impregnated with an electrolyte. Leads are connected to each foil section. The foil connected to the positive lead has an

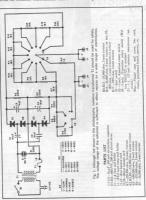
oxide coating which serves as the capacitor's dielectric. It is the thickness of this coating that determines the working voltage of the

capacitor.

While the capacitor is being used, the unide coating is preserved by chemical processes resulting from the voltage impressed to the temptal. Unfortunately when it was a them it.

is in storage, time and ambient heat take their toll and the oxide deteriorates. When the full working voltage is applied to a capacitor whose coide is weak, the latter breaks and a chest strength in placed across the

Reforming the Dielectric. The dielectric





Perf board construction may be used with operating confront panel of selected cabinet. A TV-type cheater connector is used to make the power connection, Mount perf board on suitable spacers and be sure that components on board do not make electrical contact with



of a suspect capacitor can be reformed by

The "Electrolytic Restorer" described here does this job automatically, and requires only

progress. The cost of the project is about \$14 if all parts are bought new.

an occasional look at a de voltmeter to check Construction. The prototype shown in the suffice. The schematic of the circuit is shown

THEORY OF CIRCUIT DESIGN

Diodos DI through DI and capacities CI through CI form a fell-wave voltage quadrup-lar recifier with a do output of approximately as the control of the cont

used as a high-voltage, low-current power supply. This position can be diminated if destruct.

The succession position of SI places RI. The succession position of SI places are across the output to discharge the formed capacitors, while resistors R. a. a. M. Stepp a small lead on the power supply and discharge

the power supply capacities.

During the forming process, the capacitor's resistance is low so most of the wiltage in dropped across the limiting resistor. As the exide coating in the capacitor is re-formed, less current flows through the capacitor, canning the voltage across it to increase. When the voltage series it is increase. When the voltage squals the preset voltage on ST,

in Fig. 1. Exact placement of parts is not given since dimensions are not critical and the control locations can be changed de-

perforated board. The front panel controls and jacks are mounted directly on the case cover, making sure that all tends are long enough to reach the electronics board. For safety, a 1:1 ac line isolation transformer should be used, though this is not shown in the prostopy.

Operation. The electrolytic capacitor to be reformed is connected to the output sizes, making sure that the polarities are observed. The positive side of the capacitor is connected to either JJ or J2 and the negative side to either JJ or J2 and the negative side to either J3 or J4. The de voltmeter for checking the reforming action is connected to the remaining two jacks. Make sawe that The voltmeter can be disconnected and reconnected at any time without differing the

operation.

Place S3 in the DESCLARGE position, plug that it in, and turn on the power. Nees indicator lamp II should glow. Set the desired forming rate on S2 and then rotate S3 to the working voltage of the capacitor. If the capacitor is unformed, the voltmeter will indicate a much lower voltage than that set.

Note that the voltmeter indication starts to increase quickly at first, then slows down as the dielectric forms. The rate of increase is determined by the condition of the capacitor and the setting of S2. When the s.r.ow setting



Be careful when drilling holes in the plastic front panel as it will chip easily. Neon indicator lamp is cemented in the hole, other components use hardware.



Insulated wiring is used to make interconnections. If metal case is used, make sure spacers keep connections from touching case.

is used, the operation takes lenger but the oxide formed will be of better quality. The opposite is true for the xars setting. Use the NORMAL position for most cases. When the voltage across the capacitor is approximately equal to the set on S3, put the

approximately equal to the set on \$3, put the switch on DISCHARGE and remove the capacitor. No harm will be done if the capacitor is left consected longer than required, so it is not necessary to check progress constantly.

(S2) to nessor. A current of 4 mA may be drawn continuously, and somewhat higher currents for a short period of time. (A load current of 10 mA causes a dissipation of 3 watts in the divider resistors.) The Electrolytic Restorer can also be used for a quick gone-go check of voltmeters. Comparison of voltage switch settings and voltmeter readings will reveal any gross in-

rent nower supply, set the forming rate switch



The finished front panel should be labeled as shown here. A coating of transparent plastic spray keeps lettering from getting smeared.